

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

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of

3

Complete if Known

Application Number

09/998008

Filing Date

November 28, 2001

First Named Inventor

Stephen R. Mooney

Art Unit

2667

Examiner Name

Jordan J. Hamann

Attorney Docket Number

42390P12572

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
JJH		US-4885587	12-05-1989	WEIGAND et al	
JJH		US-5604450	02-18-1997	BORKAR et al	
JJH		US-6185149	02-06-2001	FUJIOKA et al	
JJH		US-6348811	02-19-2002	HAYCOCK et al	
JJH		US-6373289	04-16-2002	MARTIN et al	
JJH		US-6437601	08-20-2002	BORKAR et al	
JJH		US-6453422	09-17-2002	DABRAL et al	
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FOREIGN PATENT DOCUMENTS

[illegible]

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				Filing Date	November 28, 2001
				First Named Inventor	Stephen R. Mooney
				Art Unit	2667
				Examiner Name	Jordan I. Hamann
Sheet	2	of	3	Attorney Docket Number	42390P12572

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.†	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code [§] (if known)			
JJH		US-2002/0095622	07-18-2002	HAYCOCK et al	
JJH		US-2003/0059036	03-27-2003	CASPER et al	
JJH		US-2004/0120405	06-24-2004	JAUSSI et al	
JJH		US-2006/0110952	05-25-2006	BORKAR et al	
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
JJH		Crisp, Richard, et al., "Development of Single-Chip Multi-GB/s DRAMs", 1997 IEEE International Solid-State Circuits Conference (ISSCC), pp. 226-227 & 461 (Feb. 6-8, 1997).	
JJH		Haycock, Matthew, et al., "A 2.5 Gb/s Bidirectional Signaling Technology", Hot Interconnects V Symposium Record, pp. 149-156 (Aug. 21-23, 1997).	
JJH		Haycock, Matthew, et al., "3.2 GHz 6.4 Gb/s per Wire Signaling in 0.18 um CMOS", 2001 IEEE International Solid-State Circuits Conference (ISSCC), pp. 62-63 & 430 (Feb. 5-7, 2001).	
JJH		Mooney, Randy, et al. "A 900 Mb/s Bidirectional Signaling Scheme", IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, pp. 1538-1543 (Dec. 1995).	
JJH		Mooney, Randy, et al. "A 900 Mb/s Bidirectional Signaling Scheme", 1995 IEEE International Solid-State Circuits Conference (ISSCC), pp. 38-39 & 337 (Feb. 15-17, 1995).	
JJH		Wilson, Howard, et al., "A Six-Port 30-GB/s Nonblocking Router Component Using Point-to-Point Simultaneous Bidirectional Signaling for High-Bandwidth Interconnects", IEEE Journal of Solid-State Circuits, Vol. 36, No. 12, pp. 1954-1963 (Dec. 2001).	

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